



EXPEDITED PROCEDURE - EXAMINING GROUP 2123

S/N 09/607,421

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|-------------|----------------------------|-----------------|--------------|
| Applicant: | Thomas A. Tetzlaff | Examiner: | Samuel Broda |
| Serial No.: | 09/607,421 | Group Art Unit: | 2123 |
| Filed: | June 30, 2000 | Docket No.: | 884.279US1 |
| Title: | DIGITAL CIRCUIT SIMULATION | | |
| Assignee: | Intel Corporation | Customer No.: | 21186 |

*OK to
enter per
RCE
3-7-05*

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the final Office Action dated 2 November 2004, Applicant requests reconsideration of the above-identified application in view of the following.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a one-month extension of the period for responding to the Office action, thereby moving the deadline for response from 2 February 2005 to 2 March 2005.